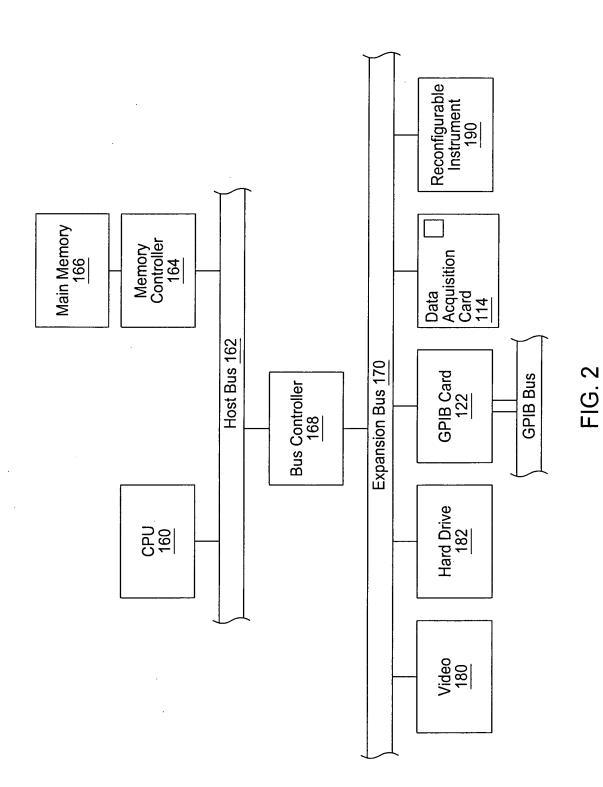
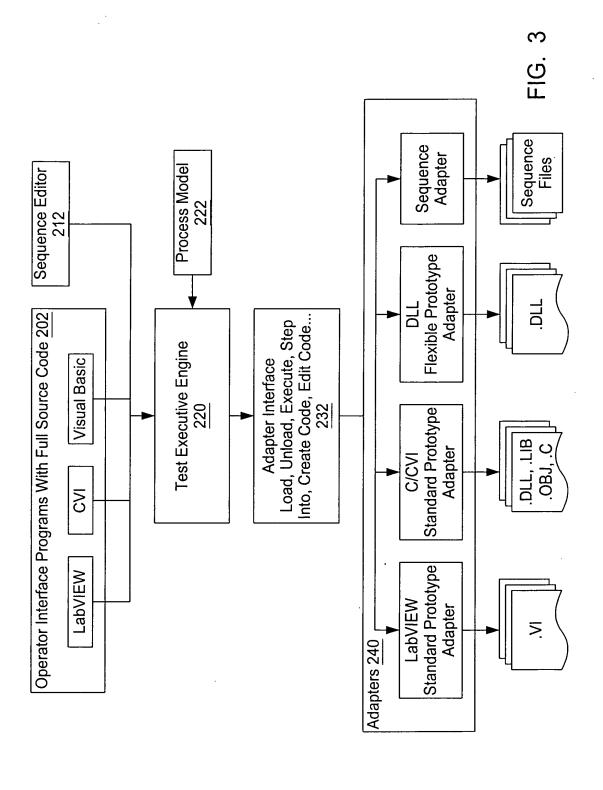


FIG. 1





| | | | | M |
|-------------------|--|--|---------------------------------------|-------------------|
| Ę | Description | Execution Flow | Comment | |
| | Numeric Limit Test, 12 <= x <= 15, volt, Call P | | | |
| | Call MainSequence in cpu.seq | Precondition | | |
| | Pass/Fail Test, Call ROMTest (computer.dll) | Precondition | | |
| RAM P. | Pass/Fail Test, Call RAMTest (computer.dll) | Precondition | | |
| ₹ video | Numeric Limit Test, 70 \leftarrow x \leftarrow 75, hertz, Call | Precondition | | |
| Keyboard Pa | Pass/Fail Test, Call KeyboardTest (computer.dll) | Precondition | | |
| ROM Diagnostics A | Action, Call ROMDiagnostics (computer,dll) | Precondition | | |
| | Action, Call RAMDiagnostics (computer.dll) | Precondition | | |
| | Action. Call VideoDiagnostics (computer all) | Precondition | | |
| stics | Action. Call KeyboardDiagnostics (computer all) | Precondition | | |
| END | | | | |
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| 9 | | | e Mode | | | | | Fallure | | |
| ing sequence f | | | tion in Interactiv | Activation | | | | ises Sequence | Errors | |
| Jption: ad when oper | l Option | ode sedu | allion Evalua | station option | ctivation | ecord Results | <u>B</u> reakpoint | ep <u>F</u> ailure Cal | nore Rum Tim | |
| | on operang sequence file | on operang sequence file | nen opening sequence file from: ion: hen sequence file is closed | | | | | | | |

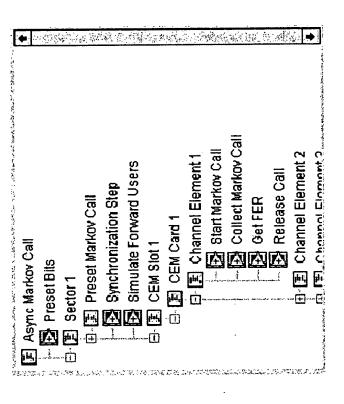


FIG. 6

Display a hierarchical view of a test executive sequence hierarchy 5<u>01</u>

Receive user input requesting hierarchical execution of a first test executive sequence displayed in the hierarchy 503

Execute the first test executive sequence in response to the user input;

wherein said executing the first test executive sequence does not include executing test executive sequences above the first test executive sequence in the hierarchy;

wherein said executing the first test executive sequence comprises testing a portion of a hierarchical system 505

create a plurality of test executive sequences in response to user input, wherein each test executive sequence maps to a sub-component of a hierarchy of system components and is operable to test the respective sub-component

701

call a first test executive sequence to test a first system subcomponent of a first level in the hierarchy, wherein the first level is not the top level of the hierarchy

703

execute the first test executive sequence without executing test executive sequences that map to system sub-components above the first sub-component in the hierarchy

705

